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APPLICANTS

Gianfranco Cerofolini, Milano, ITALY;

Giuseppe Ferla, Catania, ITALY;

** CONTINUING DATA *****

This application is a DIV of 10/199,964 07/18/2002 PAT 6,724,009

** FOREIGN APPLICATIONS *****

EUROPEAN PATENT OFFICE (EPO) 01830486.5 07/19/2001

EUROPEAN PATENT OFFICE (EPO) 01127923.9 11/23/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/29/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	ITALY	1	20	3
Examiner's Signature <i>TN</i>	Initials			

ADDRESS

00500
SEED INTELLECTUAL PROPERTY LAW GROUP PLLC
701 FIFTH AVE
SUITE 6300
SEATTLE, WA
98104-7092

TITLE

Semiconductor integrated electronic device and corresponding manufacturing method

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of